

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>1</b>	<b>L1</b>	<b>3762</b>	<b>(361/707,709,710,711,715,717,718,734,807,808,809,811,820,821,822,823,825,830,831).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>1581</b>	<b>(363/37,79,141,144,146,147).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>999</b>	<b>(307/151,154,155,156).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>6258</b>	<b>1 2 3</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>201</b>	<b>4 and @pd&gt;=20030124</b>	<b>USP AT; US-P GPU B</b>
<b>6</b>	<b>L6</b>	<b>62</b>	<b>5 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>12</b>	<b>6 and sensor\$1</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>Search Text</b>	<b>DBs</b>
<b>8</b>	<b>L8</b>	<b>50</b>	<b>6 not 7</b>	<b>USP AT; US-P GPU B</b>